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## **FIELD MAINTENANCE PRINT SET**

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GENERAL PURPOSE DMA INTERFACE	D-UA-M8716-Ø-
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GENERAL PURPOSE DMA INTERFACE	B-PL-M8716-Ø-
CABLE, BCØ5L	D-UA-BCØ5L-Ø-
SHIPPING LIST	A-PL-DR11-W

**UNIT VARIATIONS  
COVERED BY THIS  
PRINT SET**

# DRII-W

## Field Maintenance Print Set

# Digital Equipment Corporation

PRINT SET ORDER  
NUMBER MP00693

**DRB 124**

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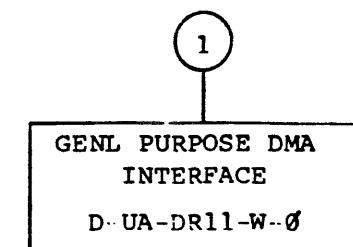
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FOR FIELD MAINTENANCE PRINT SET  
REFER TO B-TC-DR11-W-1

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DR11-W  
NUMBER REV.  
SIZE CODE B DD



TITLE GENERAL PURPOSE DMA INTERFACE	SHEET 2 OF 3	SIZE CODE B DD	NUMBER DR11-W	REV
--	--------------	----------------	---------------	-----

TYPE: E ELECTRICAL  
M MECHANICAL  
E/M ELECTRO/MECHANICAL

digital

TITI

## GENERAL PURPOSE DMA INTERFACE

SHEET 3 OF 3

SIZE CODE  
**B DD**

**NUMBER**

REV

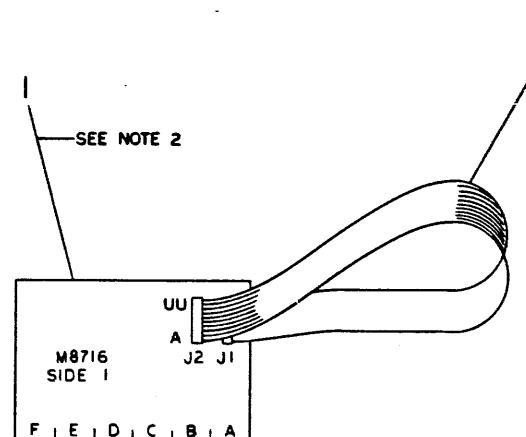
**DRB 108A**

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8 7 6 5 4 3 2 1 DRA-DRII-W-0

NOTES:

1. THE M8716 MODULE IS PLUGGED INTO ANY SPC SLOT THAT IS WIRED FOR ALL UNIBUS SIGNALS.
2. THE NPR GRANT JUMPER (CA1 TO CBI) MUST BE REMOVED BEFORE INSTALLING THE M8716 (ITEM 1). THIS JUMPER MUST BE REPLACED IF THE M8716 IS REMOVED FROM THE SYSTEM.



REF.	GENL PURPOSE DMA INTERFACE	D-AR-DRII-W-2	7
I	OUTER BOX	9906088-07	6
I	COURGATED SLEVE	9906089-07	5
I	DRII-W SHIPPING LIST	A-PL-DRII-W	4
I	MODULE BOX	9905816-00	3
I	BC05L CABLE,JUMPER	D-UA-BC05L-IC	2
I	GENL PURPOSE DMA INTERFACE	D-UA-M8716-0-0	1

QTY	DESCRIPTION		DWL/PART NO.	ITEM NO.		
	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES					
ANGLES 30° 30°	CLASS OF ACCURACY	NORMAL DIMENSION RANGE INCHES				
		0.000	0.032	0.125	0.500	
SURFACE QUALITY	IN	CHECK ONE		MEDIUM		0.004
		✓	MEDIUM	0.004	0.008	
QUANTITY & VARIATION		MEDIUM		PREFERRED		0.004
SERIALIZED		0.012		0.016		0.001
DRAWN BY R.Kognal 2-1-78	FIRST USED ON		DRII-W		D-AR-DRII-W-2	
CHKD BY	TITLE		D-AR-DRII-W-2		D-AR-DRII-W-2	
ENG'D BY	PROL ENG'D BY		101-0-0		101-0-0	
REV'D BY	PROD. BY		101-0-0		101-0-0	
DO NOT SCALE DWG		NEXT HIGHER ASY.		MATERIAL SEE PARTS LIST		B-00-DRII-W
FINISH		SCALE		SIZE CODE		D UA DRII-W-0
SEE PARTS LIST		+		NUMBER		REV.
FINISH		+		SHEET		DRA-DRII-W-0
SEE PARTS LIST		+		OF 1		DIST.
FINISH		+		1		1

INSTRUMENTATION  
REVISION  
CHARGE NO.  
DATE

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**DRB 128**



**USED ON OPTION/MODEL**

DRN. R. KOPPEN

CHK'D

ENG

Donald Trump  
PROD-11

W.E. Mayes.

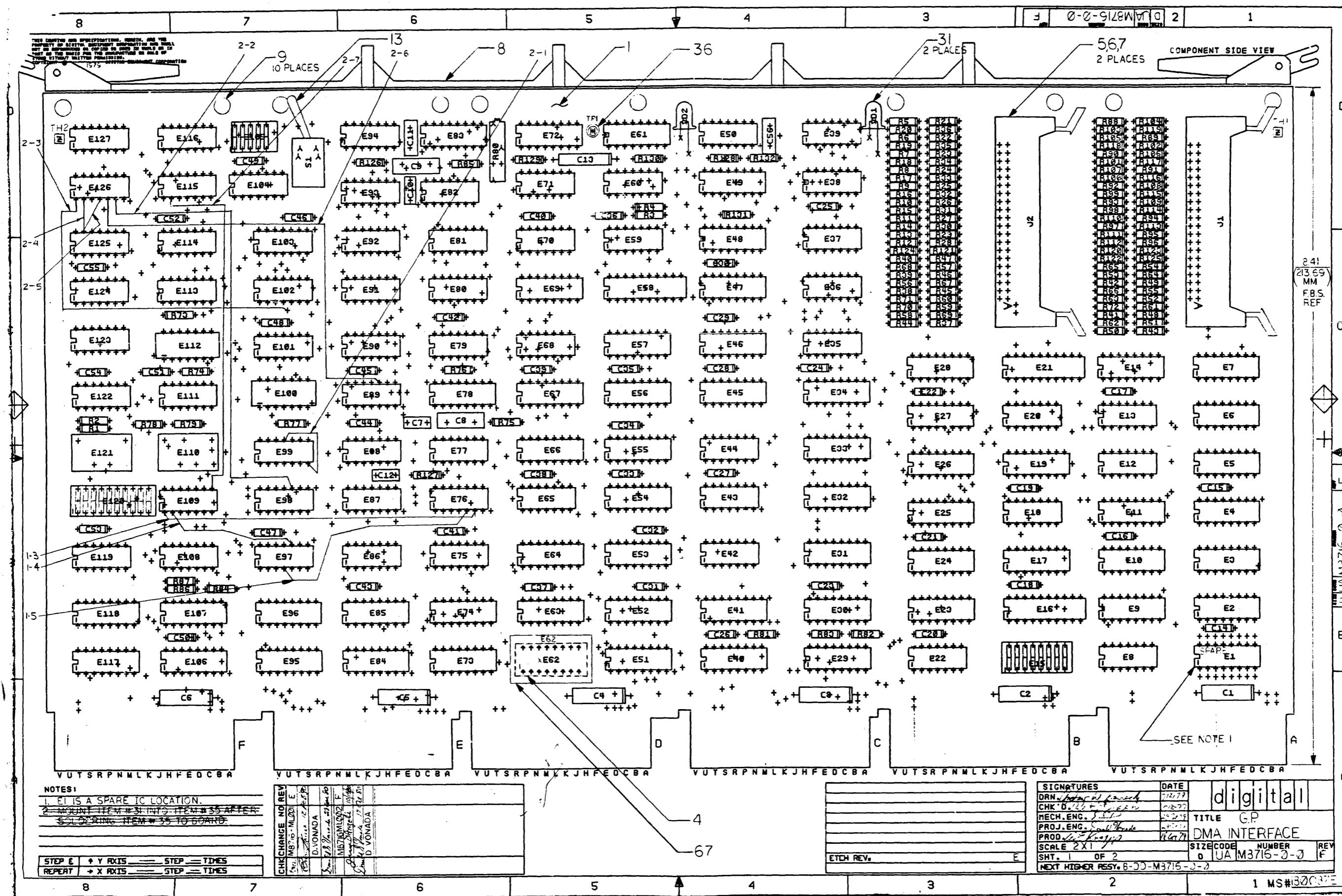
TITLE G.P.  
DMA INTERFACE

SIZE	CODE	NUMBER
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**B DD** M8716-Ø

SHEET OF

ML-1



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### REWORK INSTRUCTIONS:

- ETCH CUTS SIDE 2:
- 1-1 REFER TO D-EC-5013369-0-0
  - 1-2 REFER TO D-EC-5013369-0-0
  - WIRE ADDS
  - 1-3 FROM E76-6 TO E109-1.
  - 1-4 FROM E109-2 TO E97-10
  - 1-5 FROM E76-5 TO E97-4

### WIRE ADDS

- 2-1 FROM E99-11 TO E99-7
- 2-2 FROM E126-6 TO E98-10
- 2-3 FROM E126-1 TO PLATE THRU HOLE AT E102-4
- 2-4 FROM E126-2 TO E125-13
- 2-5 FROM E126-4 TO E125-13
- 2-6 FROM E126-5 TO E89-10
- 2-7 FROM E115-2 TO E109-8

### ETCH CUTS SIDE 1

- 2-1 REFER TO D-EC-5013369-0-0
- 2-2 REFER TO D-EC-5013369-0-0

### ETCH CUTS SIDE 2

- 2-3 REFER TO D-EC-5013369-0-0

REVISIONS		
CHK	CHANGE NO.	REV.

8 7 6 5 4 3 2 1

TITLE  
G.P. DMA INTERFACE

SIZE CODE  
D UA M8716-0-0 REV. F

SCALE // SHEET 2 OF 2 DIST. \_\_\_\_\_

8 7 6 5 4 3 2 1

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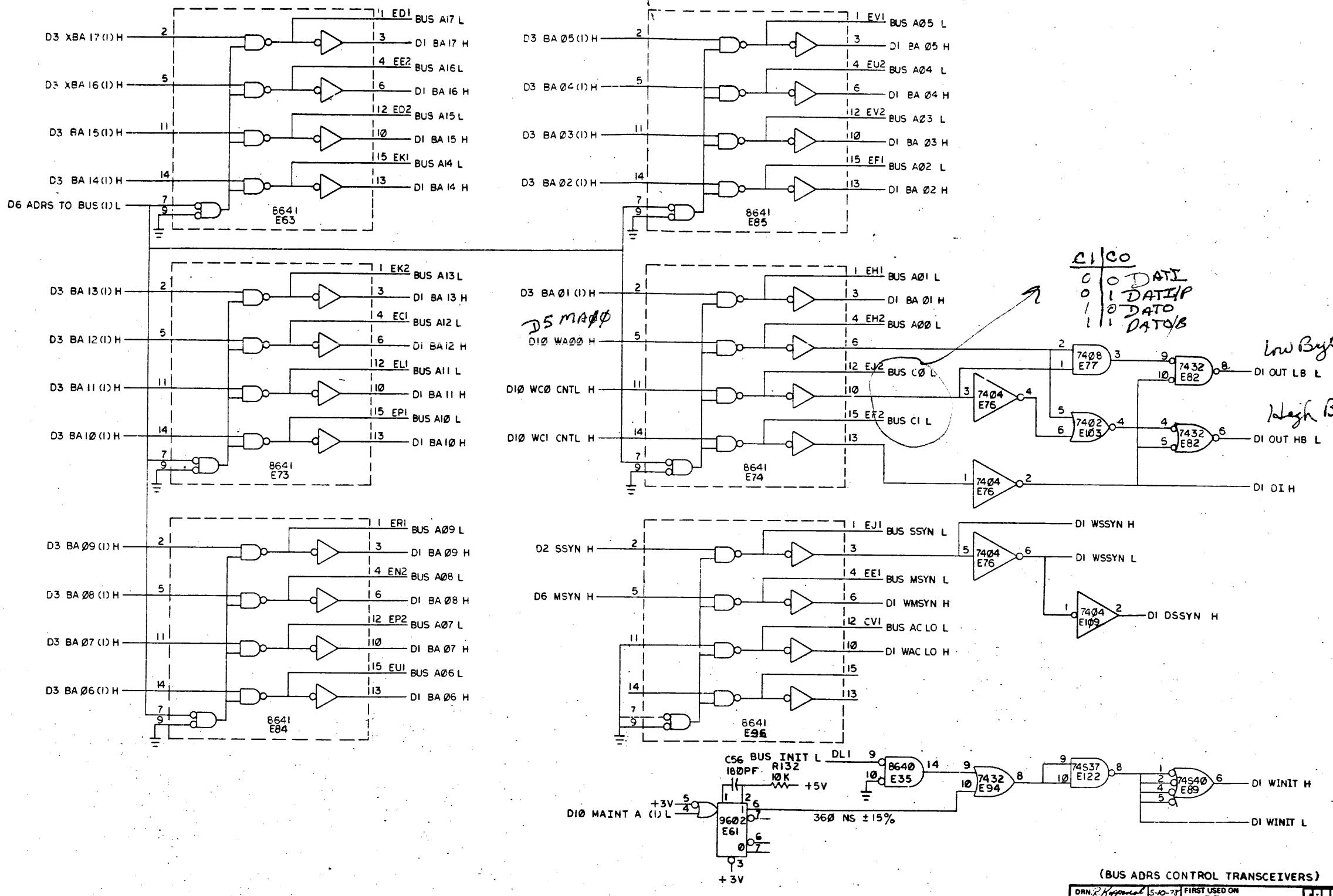
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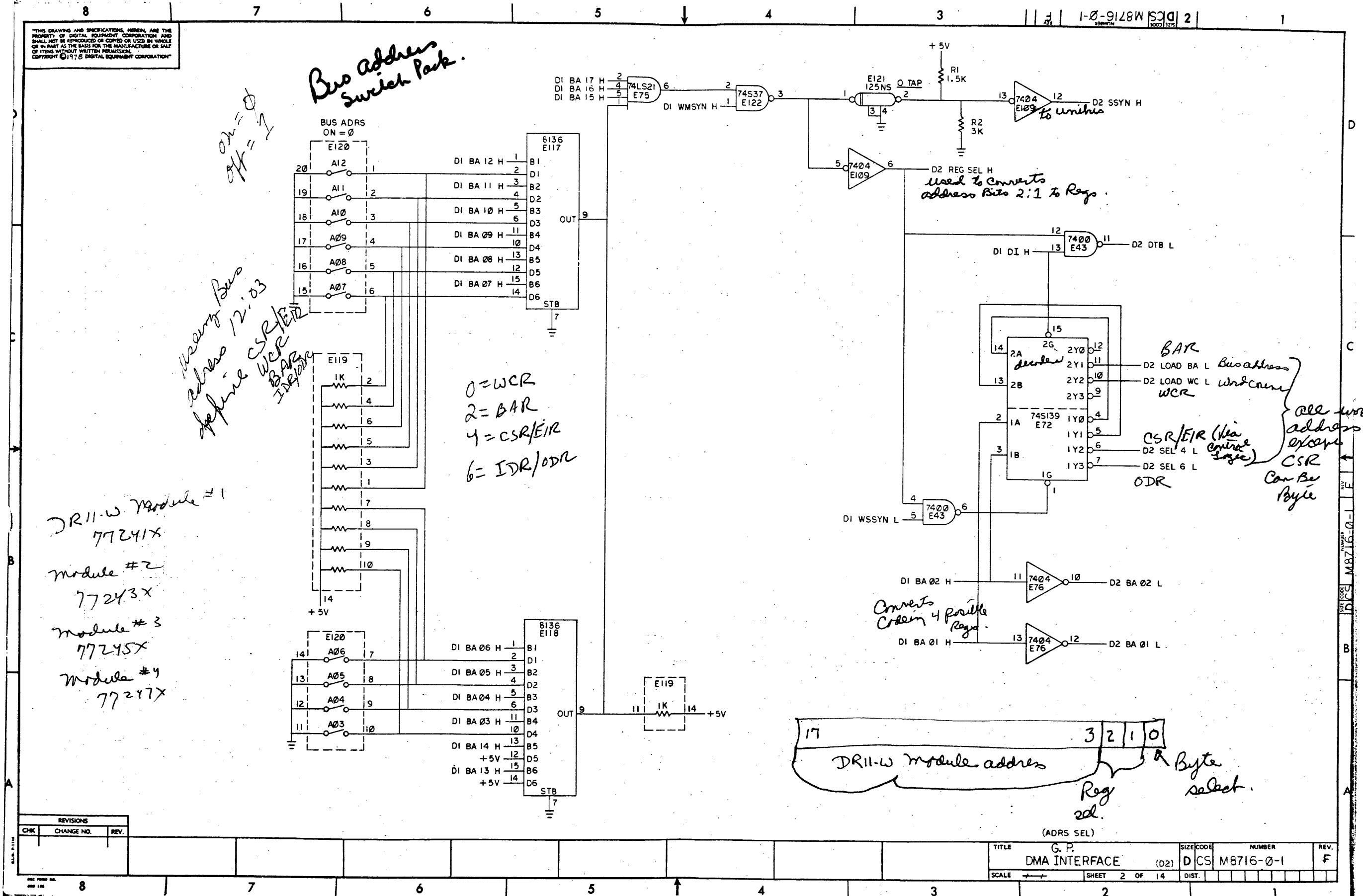
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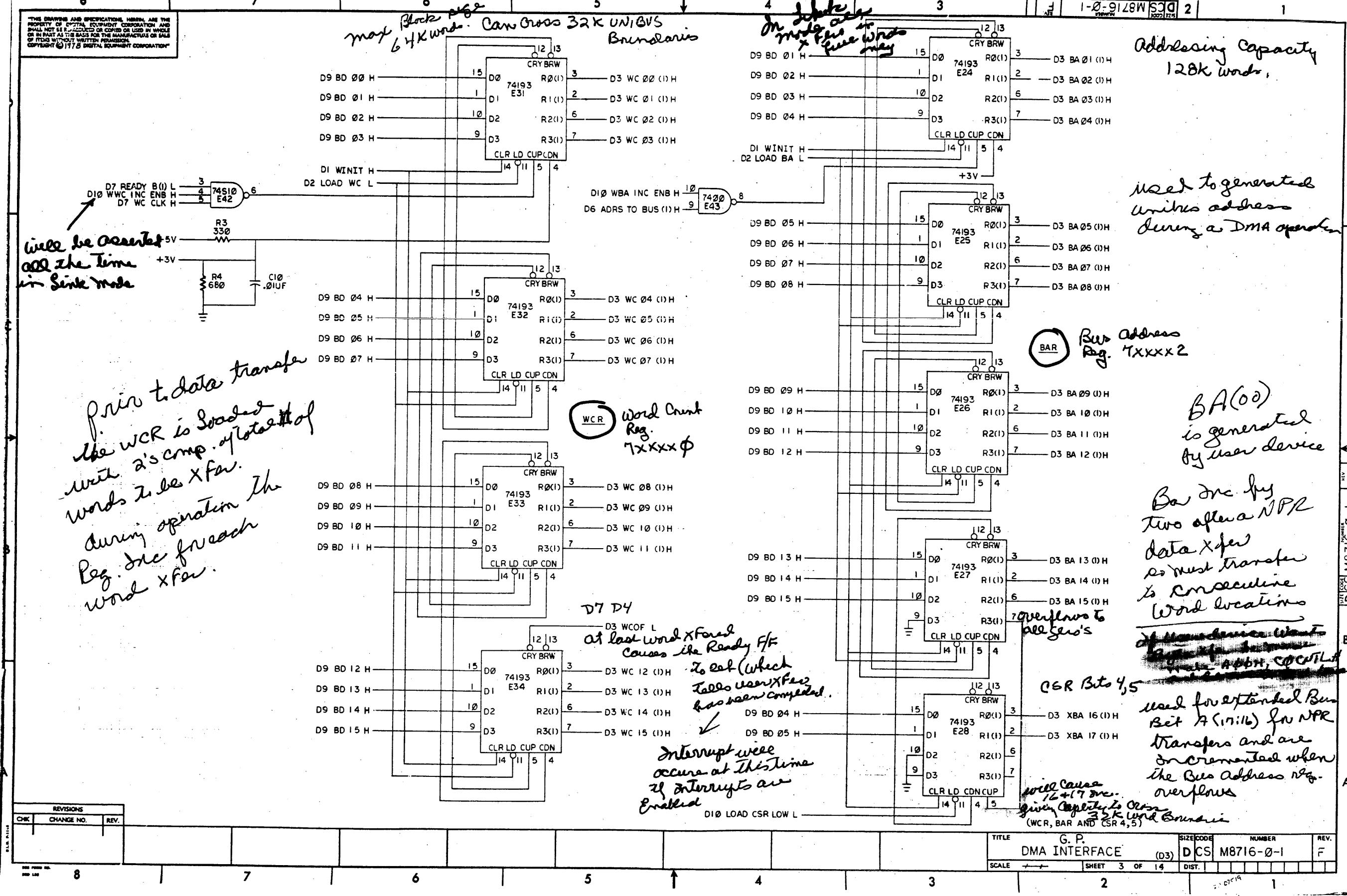
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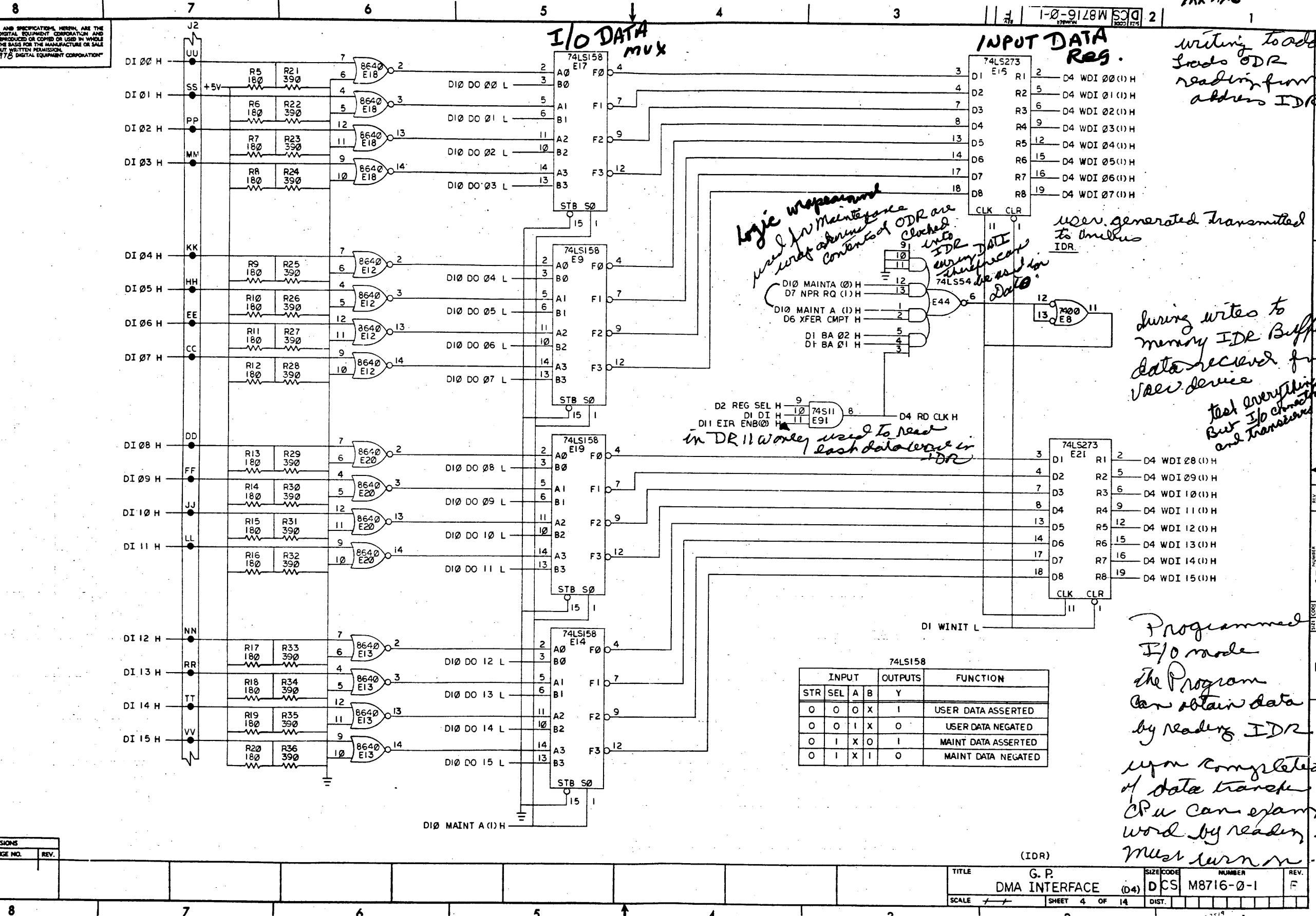
REVISIONS	
CHG	CHANGE NO.
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3	ML003
4	ML004
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9	ML009
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11	ML011
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I input/output data reg  
some address 7xx Xx6

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2 Maintenance modes  
Logic wraparound  
Cable wraparound.

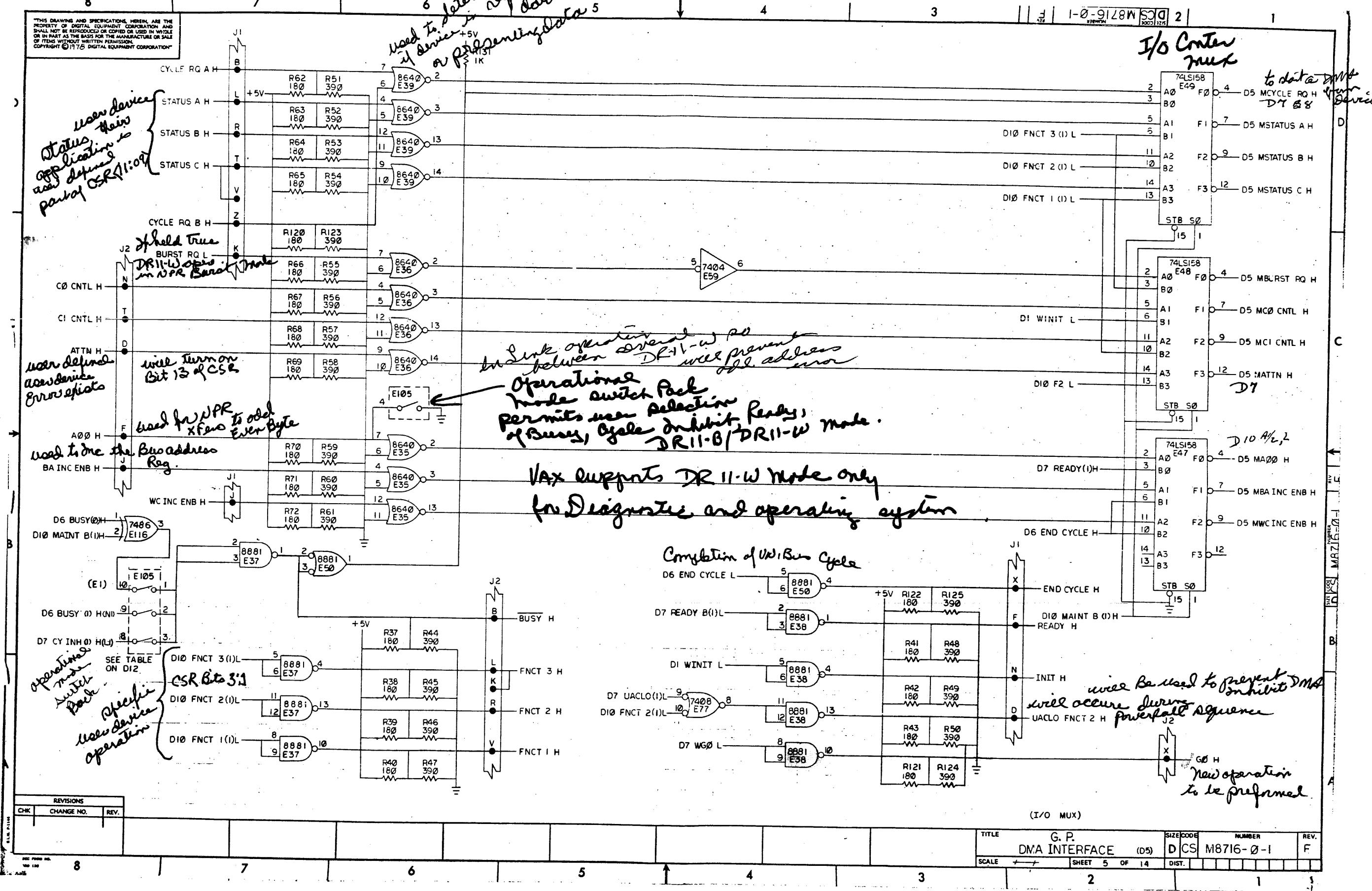
Logic wraparound  
Unibus data is applied to DR 11-W  
Then gated Back To Unibus for Comparison Checking

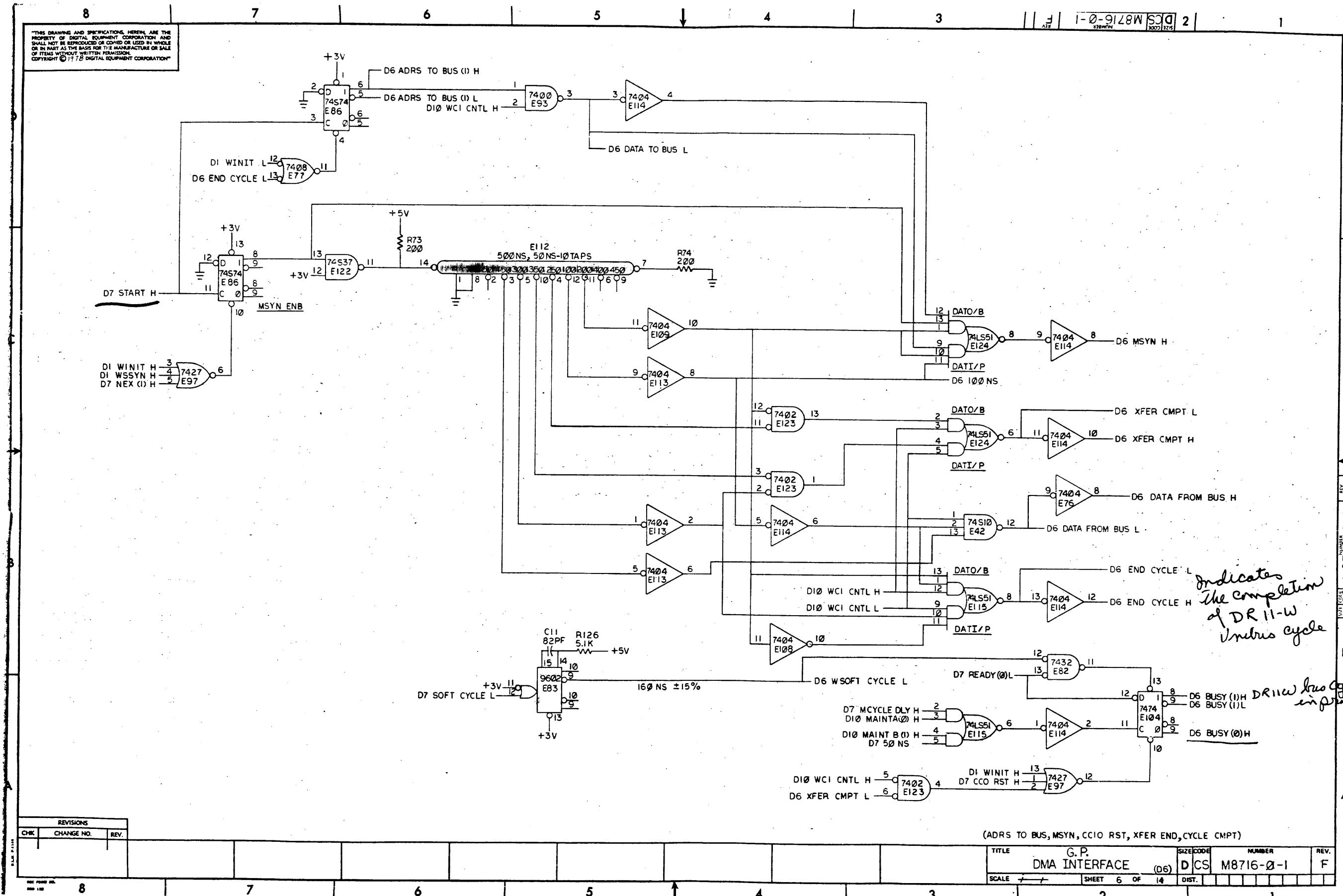
test everything  
But I/O connected and transferred BUS → ODR → IDR → IDR28s  
DATI  
DATO

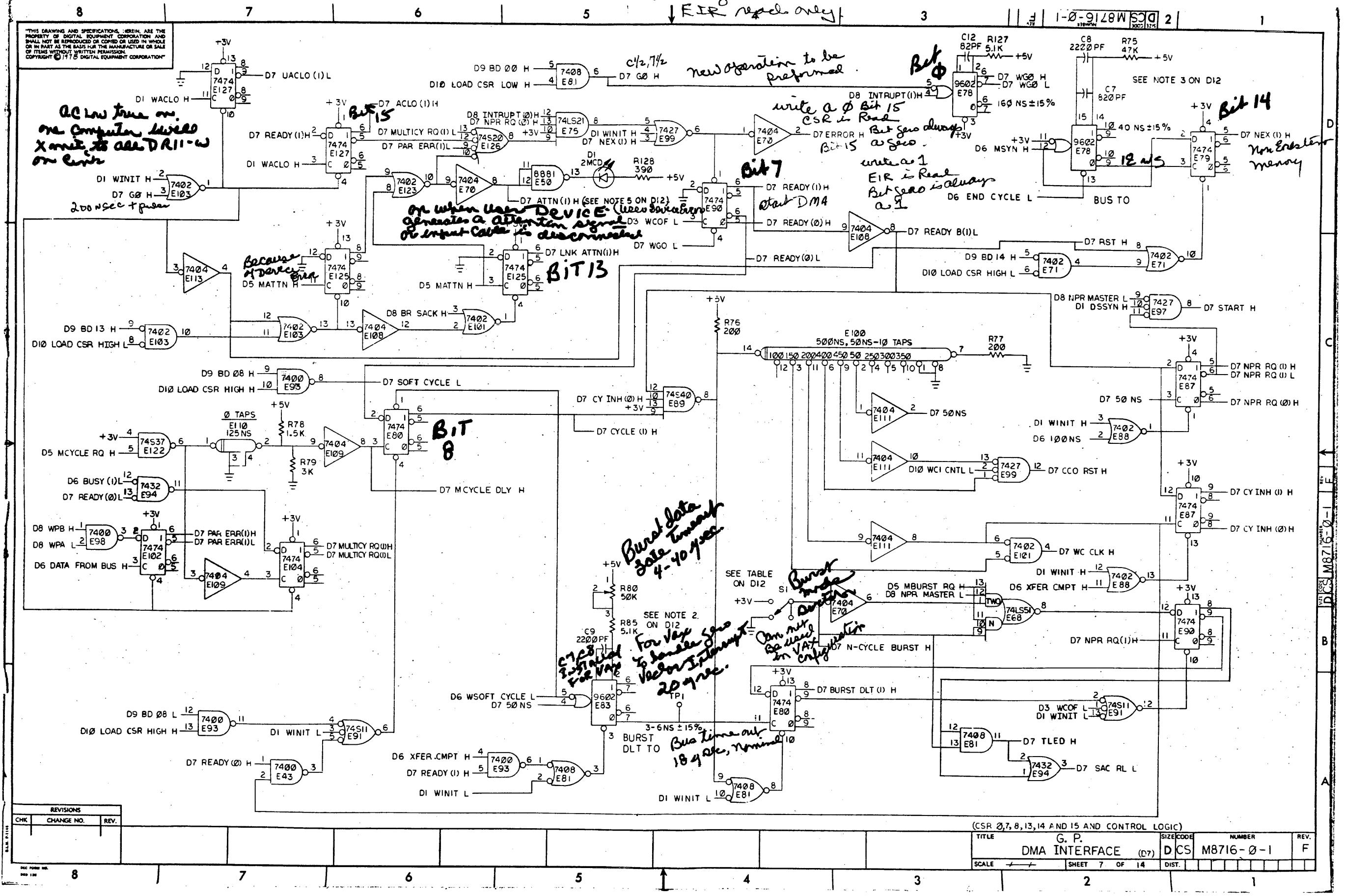
may have to do some testing  
during this testing

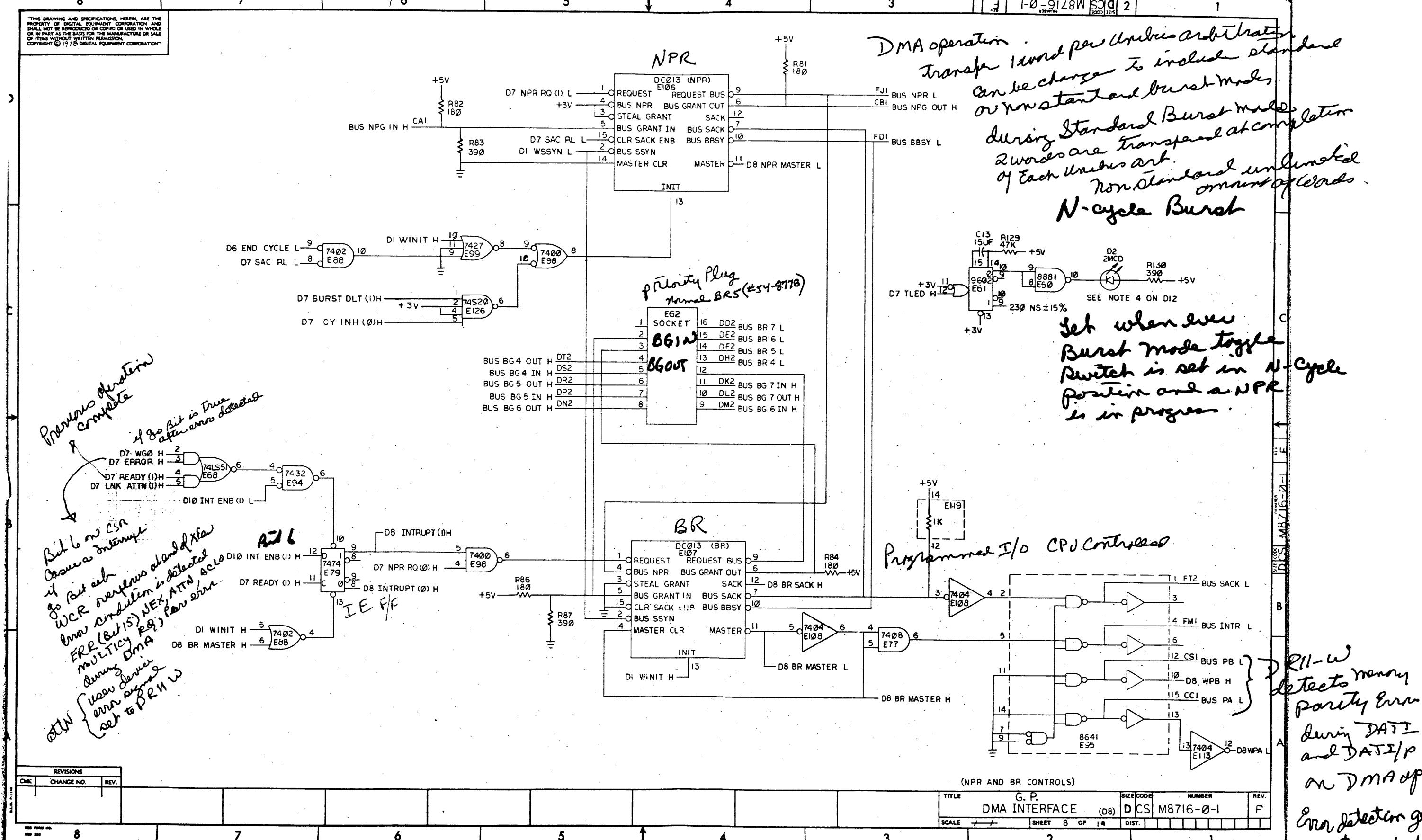
Programmed I/O mode  
The Program can obtain data by reading IDR

upon completion of data transfer  
CPU can examine last word by reading IDR  
Must return to bit 15 of CSR to see this





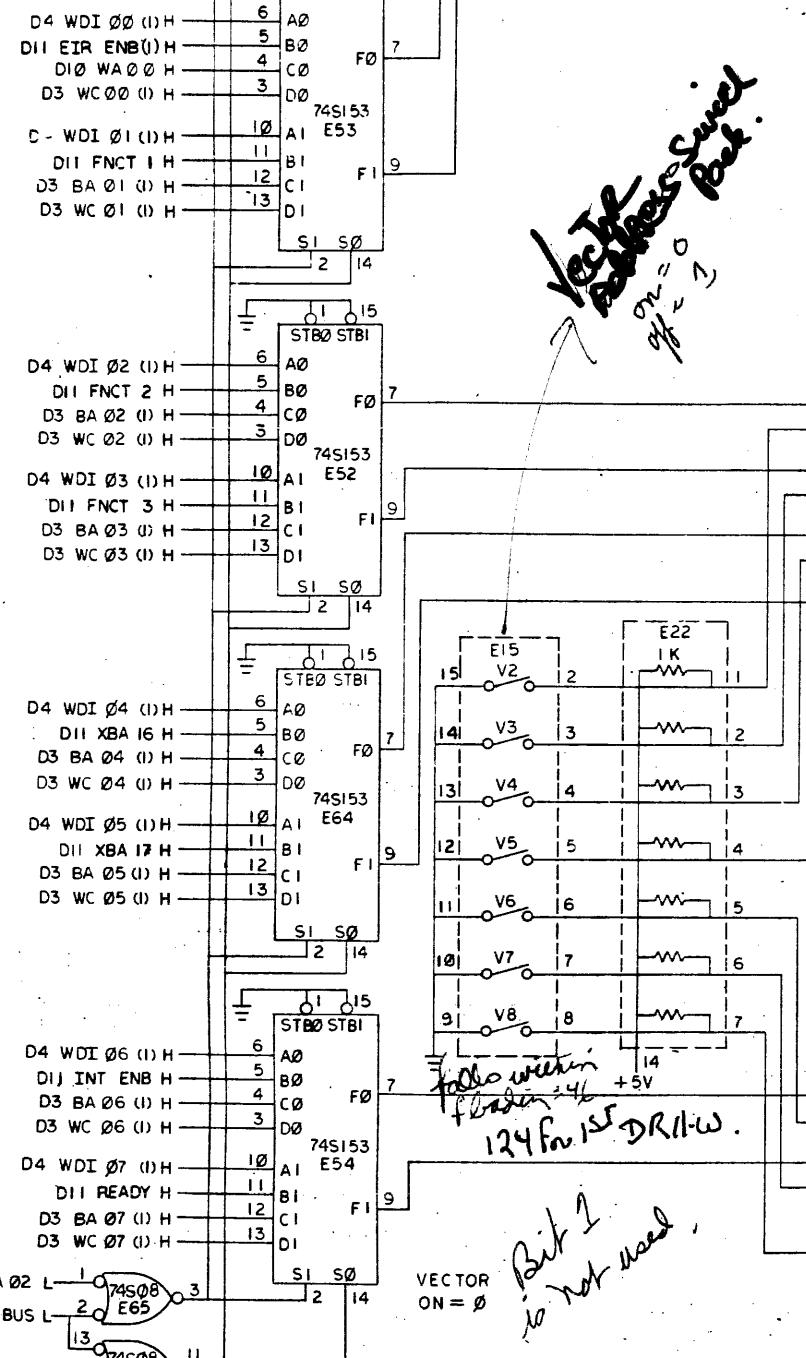




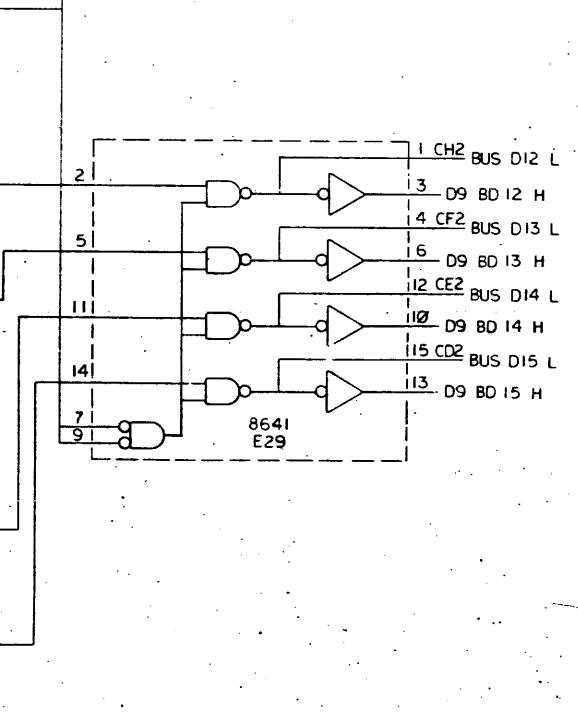
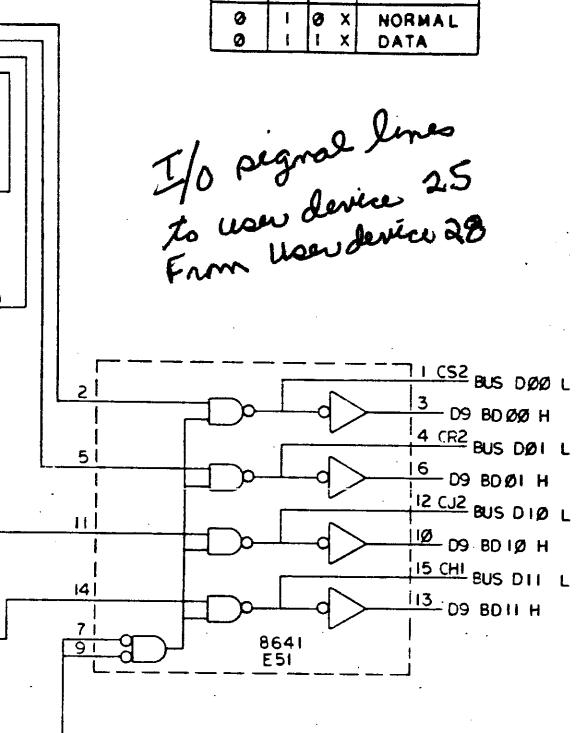
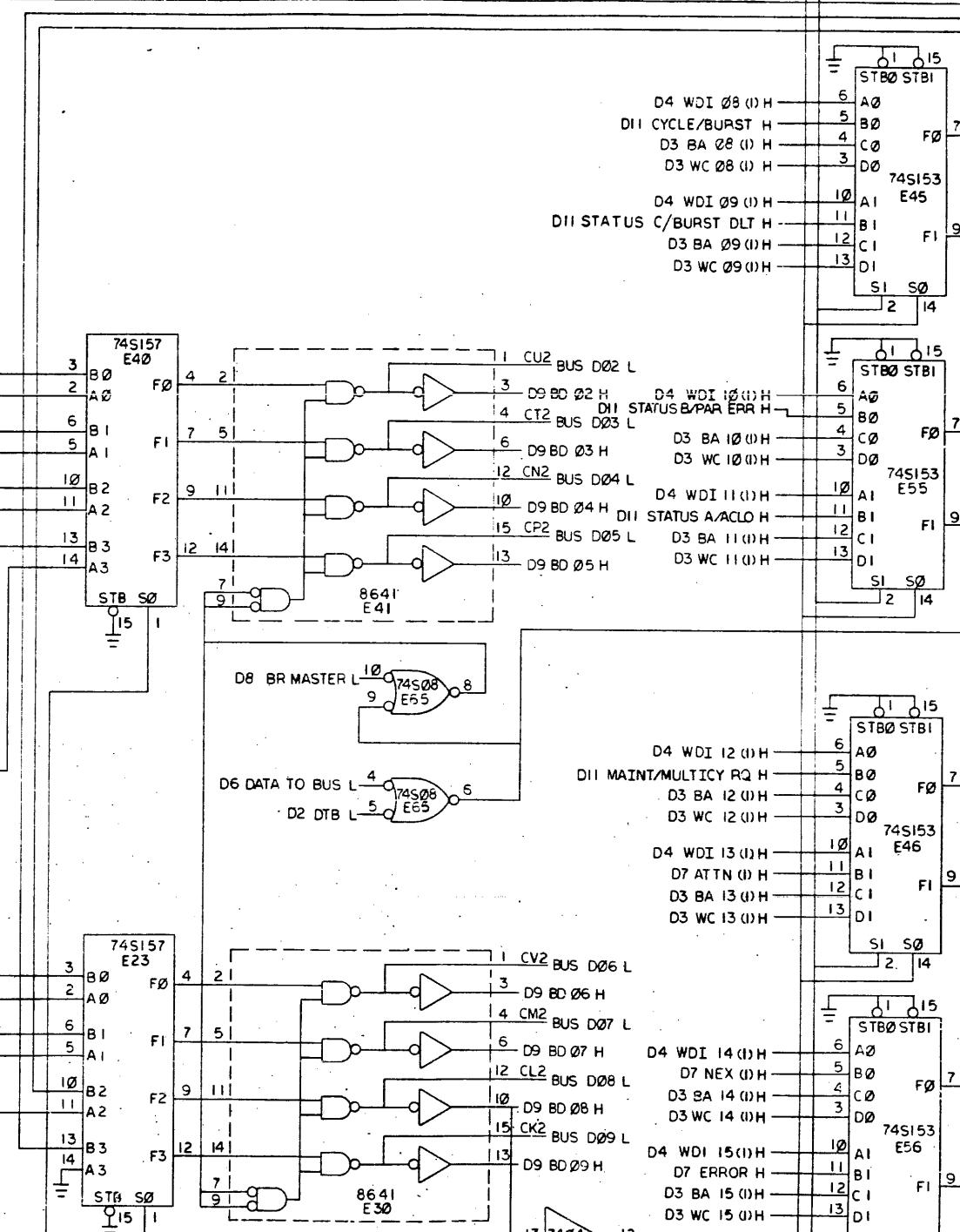
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STB	S0	B	A	F OUTPUT
0	0	X	0	VECTOR
0	0	X	1	
0	1	0	X	NORMAL DATA

I/O signal lines  
to user device 25  
From user device 28



D8 BR MASTER L



REVISIONS	CHK	CHANGE NO.	REV.	TITLE	G. P.	SIZE CODE	NUMBER	REV.
		1		(BUS I/O TRANSCEIVERS AND MUX)	DMA INTERFACE (D9)	DCS	M8716-0-1	F

SCALE / / SHEET 9 OF 14 DIST. / /

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Digital  
Data  
Out  
JTP  
Reg

REVISIONS		
CHK	CHANGE NO.	REV.

8

6

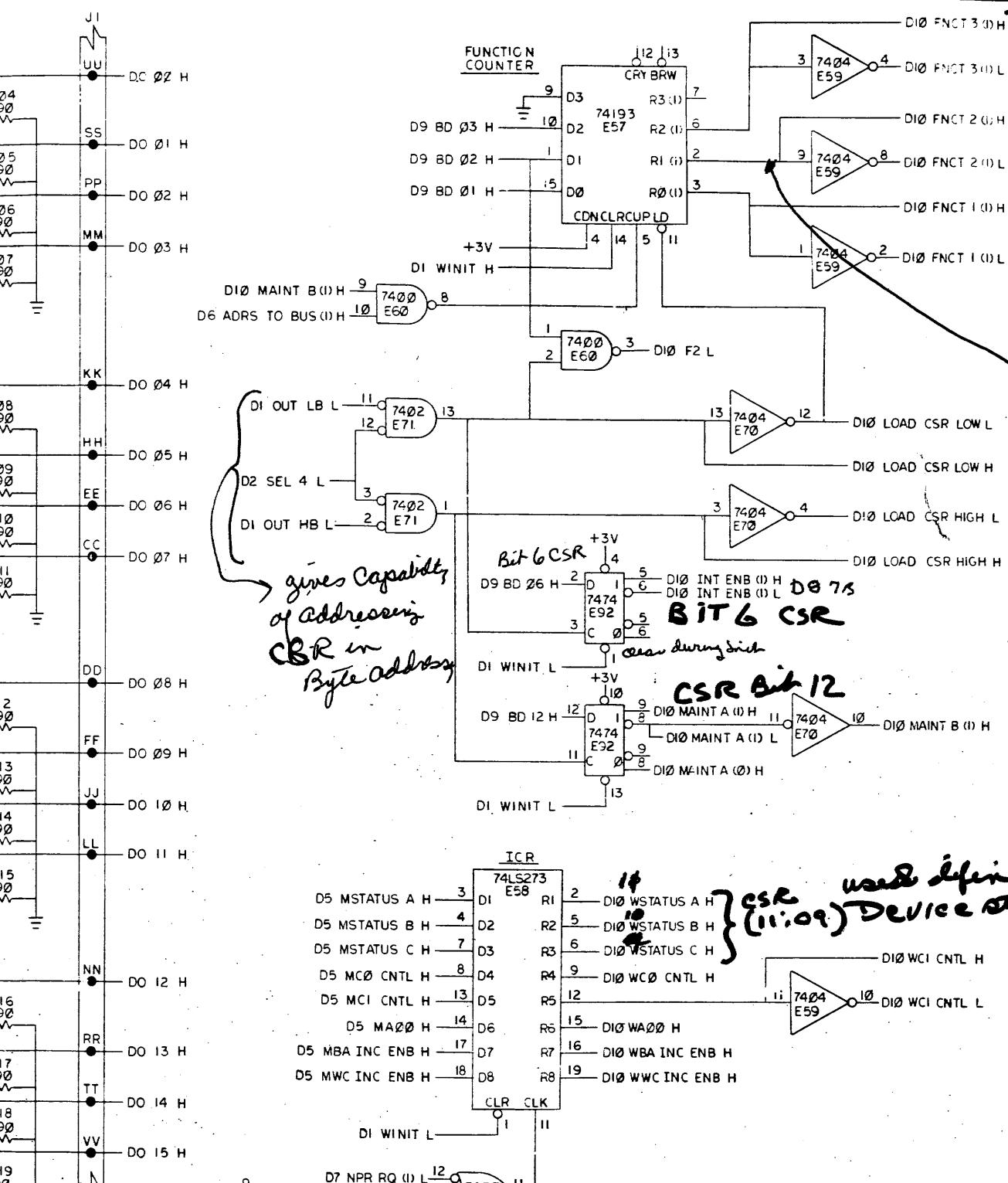
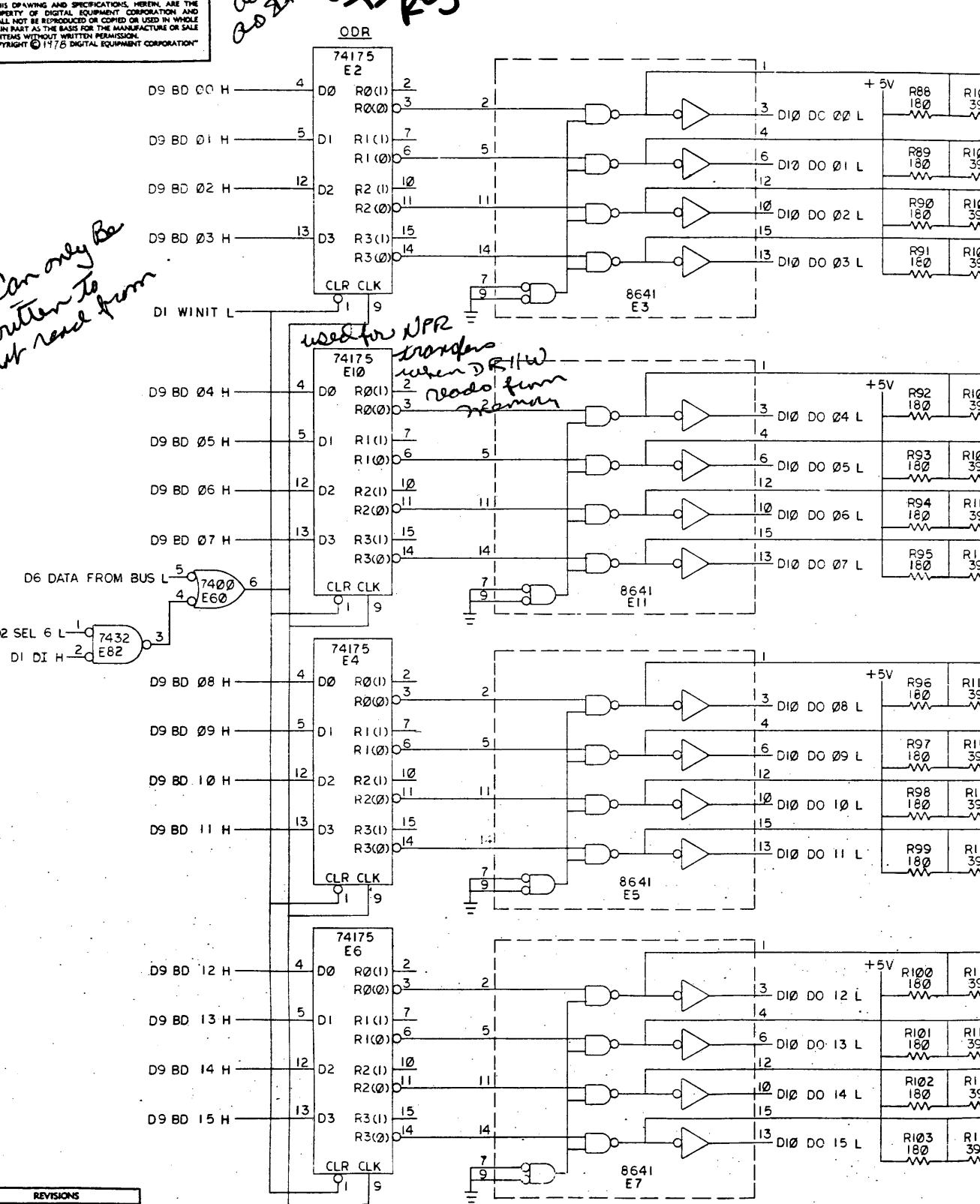
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(CDR, ICR, FUNCTION COUNTER AND CSR 1, 2, 3, 5, 9, 10, 11 AND 12)

TITLE	G.P.	SIZE CODE	NUMBER	REV.
DMA INTERFACE (D10)	D CS	M8716-0-1		

SCALE SHEET 10 OF 14 DIST. 1

CSR (3:1)

user defined  
tell what he want to do theory DR11-W

setting CSR bit 2 in writing DR11-W  
sets both CSR bit 10 and CSR bit 13 in receiving DR11-L

Bit 13 attr.  
when set will set Bit 15 EBBH which will generate a interrupt if clear during disk

Bit 6 Interrupt Enable Bit is set.

CSR used define Device Status Bits

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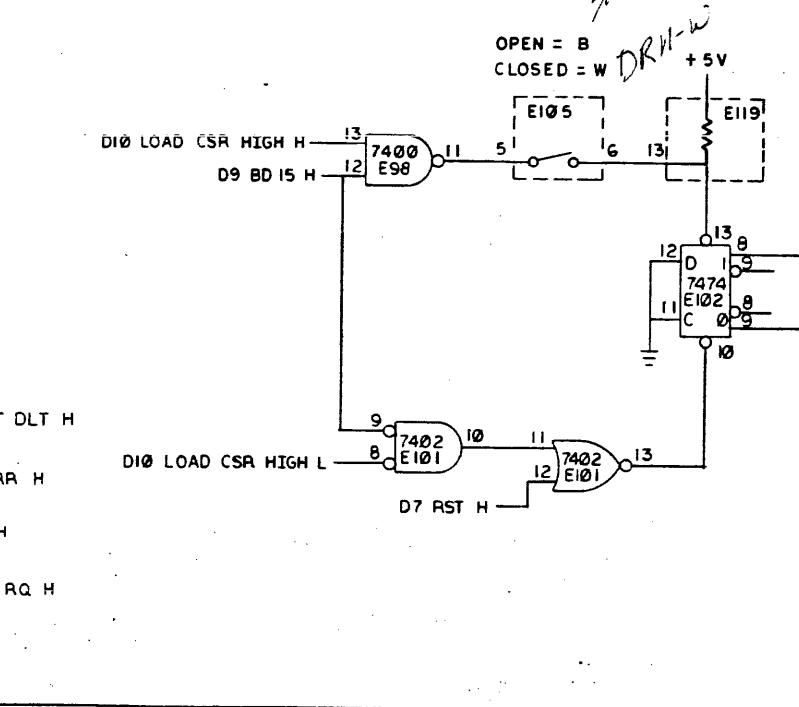
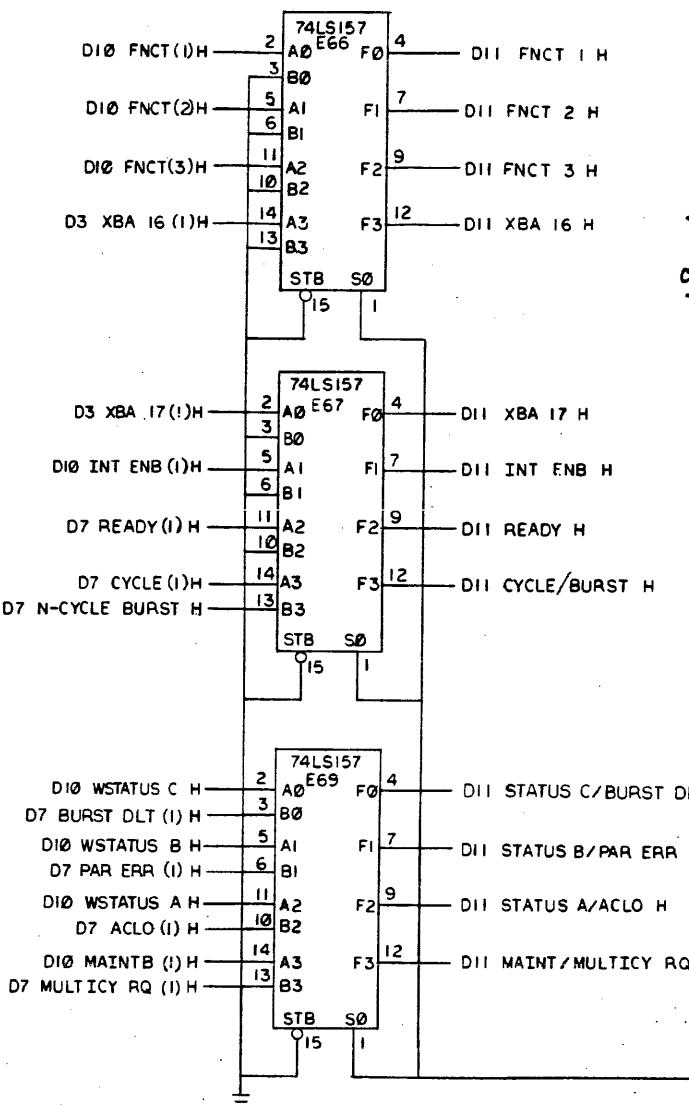
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REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. 8  
DOD 100

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TITLE G.P.  
DMA INTERFACE (D)  
SCALE 1/1 SHEET 11 OF 14 DIST. 1  
SIZE CODE DCS NUMBER M8716-0-1 REV. E

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SPARES

## **NOTES :**

1. ALL RESISTORS ARE 1/4 W, 5% UNLESS OTHERWISE SPECIFIED.
  2. THE BURST DLT TIME OUT IS USED FOR NPR BURST MODE ONLY AND MAY BE ADJUSTED BETWEEN 3 AND 30  $\mu$ S TO ACCOMMODATE INPUT DATA RATE. IT IS TYPICALLY ADJUSTED TO 10  $\mu$ S TO RUN WITH MOST PDP-11 FAMILY OF PROCESSORS.
  3. THE BUS TIME OUT MAY BE CONFIGURED FOR UNIBUS-II, UBA OF VAX AND 11/74. FOR UNIBUS-II TIME OUT C8 MUST BE CUT. FOR UBA OF VAX AND 11/74, BOTH C7 AND C8 MUST BE INSTALLED.
  4. THE LED IS LIT WHEN PULSED BY ONE-SHOT. THE ONE SHOT IS TRIGGERED WHEN BI IS IN N-CYCLE POSITION, BURST RQ IS ASSERTED AND AN NPR OPERATION BEGINS.
  5. THE LED IS LIT WHEN USER ATTN SIGNAL IS STUCK ASSERTED OR USER CABLE IS NOT CONNECTED TO MODULE CONNECTOR.

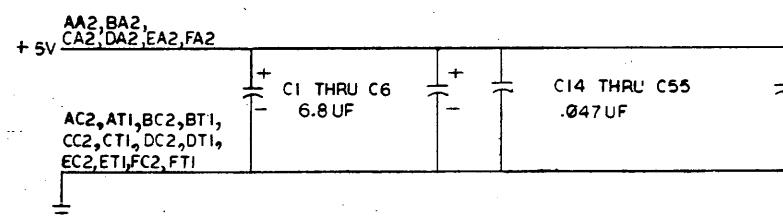
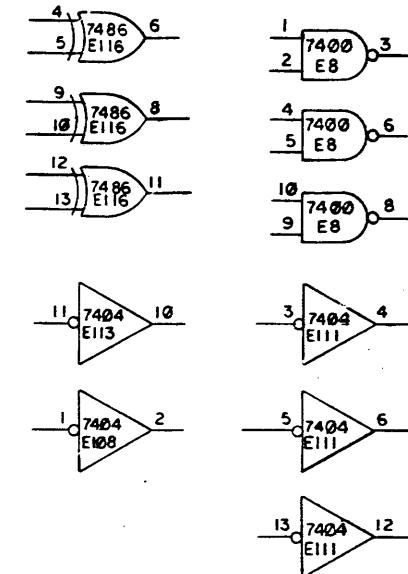


TABLE I		
POWER AND GND TABLE		
IC TYPE	GND	+5V
14 PIN IC'S	PIN 7	PIN 14
16 PIN IC'S	PIN 8	PIN 16
8640	PIN 1	PIN 8
74 LS273	PIN 10	PIN 20

TABLE II						
SWITCH MODES OF OPERATION	A00	MODE B-W	BUSY		CY INH	BURST BI *
	4	5	E1	N1	I2	L3
USER DEVICE		USER SELECT	ON	OFF	OFF	USER SELECT
MAINT, CABLE AND DRII-W TO DRII-W LINK	ON	USER SELECT	OFF	ON	OFF	USER SELECT
DRII-W TO DRVII-B LINK	ON	USER SELECT	OFF	OFF	ON	USER SELECT

\* UP POSITION FOR N-CYCLE BURST  
AND DOWN FOR TWO-CYCLE BURST



REVISIONS		
CHK	CHANGE NO.	REV.

DOC FORM NO. 8  
GPO 1964  
1

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1-0-2 DCS M8716-0-1

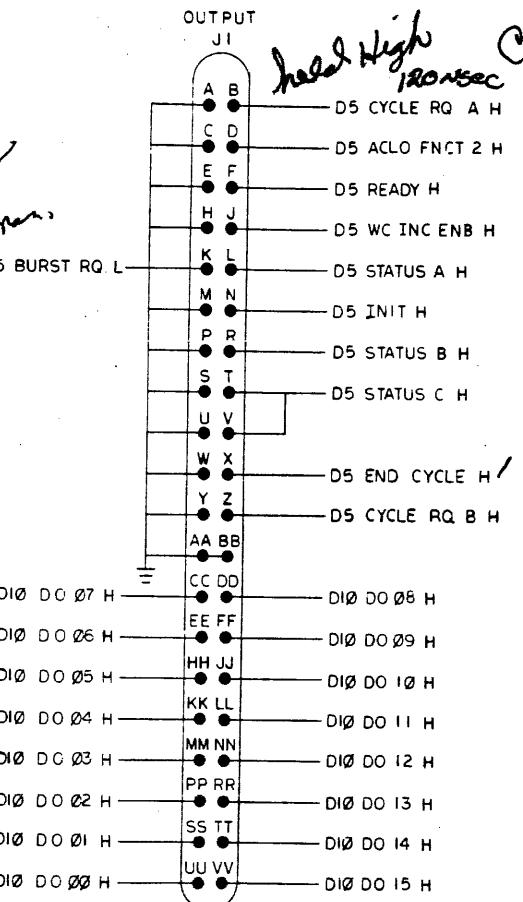
*Cable wrapped*

*B COSL test cable  
is installed between J1 output  
J2 input so that data  
can be looped back to  
module*

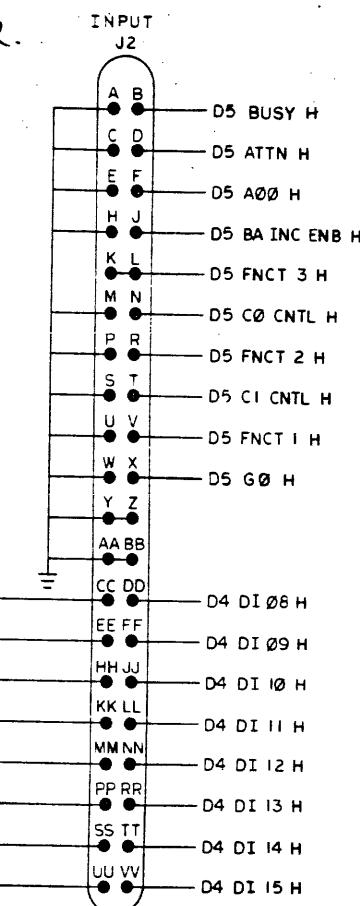
*This checks out Connectors  
and Bus Transceivers*

*Page 3-1  
test man.*

*25 lines* FOR REFERENCE ONLY



*28 lines*



*Normal hook up  
approved Interface Cable*

*B COSL -xx*

*Cable impedance 120Ω  
Max length .50 ft.,  
1524 cm*

*Page 3-3  
tech manual*

*Start a DMA J1  
END cycle H, G0 H, BusyH,  
Ready H go to user device*

*user device response  
with*

*D I (15.00) 14 - J2  
also*

*C0 CNTL H  
C1 CNTL H*

*Cycle Pg A (or B)*

*WC INC ENB H*

*BA INC ENB H*

*BVRST RQ L*

REVISIONS		
CHK	CHANGE NO.	REV.

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(I/O CONNECTOR PIN ASSIGNMENT)

TITLE G.P.  
DMA INTERFACE (D13) DCS M8716-0-1

SCALE 1/1 SHEET 13 OF 14 DIST.

REV. F

SEARCHED AND SERIALIZED, JUN 19 1980  
FBI - MEMPHIS

DY Source	ED	01	02	03	04	05	06	07	08	09	10
RO CIR H	ED3	-	-	2	-	-	-	-	-	-	-
WOI 15C) H	ED1	-	-	-	1	-	-	-	-	-	-
WOI 14C) H	ED1	-	-	-	1	-	-	-	-	-	-
WOI 13 (1) H	ED1	-	-	-	1	-	-	-	-	-	-
WOI 12 (1) H	ED1	-	-	-	1	-	-	-	-	-	-
WOI 11 (1) H	ED1	-	-	-	1	-	-	-	-	-	-
WOI 10 (1) H	ED1	-	-	-	1	-	-	-	-	-	-
WDI 09 (1) H	ED1	-	-	-	1	-	-	-	-	-	-
WDI 08 (1) H	ED1	-	-	-	1	-	-	-	-	-	-
WDI 07 (1) H	ED2	-	-	-	1	-	-	-	-	-	-
WDI 06 (1) H	ED2	-	-	-	1	-	-	-	-	-	-
WDI 05 (1) H	ED2	-	-	-	1	-	-	-	-	-	-
WDI 04 (1) H	ED2	-	-	-	1	-	-	-	-	-	-
WDI 03 (1) H	ED2	-	-	-	1	-	-	-	-	-	-
WDI 02 (1) H	ED2	-	-	-	1	-	-	-	-	-	-
WDI 01 (1) H	ED2	-	-	-	1	-	-	-	-	-	-
WDI 00 (1) H	ED2	-	-	-	1	-	-	-	-	-	-

D7 SOURCE	D1	D2	D3	D4	D5	D6	D7	D8
ACLO (I) H	D6	-	-	-	-	-	-	-
ATTN (I) H	D5	-	-	-	-	-	-	-
SORTS	C3	-	-	-	-	1	3	-
BURST DLT (I) H	A2	-	-	-	-	1	-	-
CLCRST H	B2	-	-	-	-	1	-	-
CYCLE (I) H	C5	-	-	-	-	-	2	-
CY INH (I) H	B1	-	-	-	-	1	-	-
ERROR H	D3	-	-	-	-	1	1	1
GO H	D4	-	-	-	-	-	3	-
LWK ATTN (I) H	C5	-	-	-	-	-	1	1
MULTICR RQL (I) H	B6	-	-	-	-	-	1	-
MULTICR RQL (L) H	B6	-	-	-	-	-	2	-
N-CYCLE BURST H	C3	-	-	-	-	-	4	-
PER (I) H	D1	-	-	-	-	1	2	1
NPA RQ (O) H	C7	-	-	-	-	-	2	1
NPA RQ (I) H	C1	-	-	-	-	1	-	2
NPA RQ (I) L	C1	-	-	-	-	-	2	1
PRA ERA (I) H	B7	-	-	-	-	-	1	-
PRA ERA (I) L	B7	-	-	-	-	-	2	-
READY (O) H	D4	-	-	-	-	-	-	2
READY (I) H	D4	-	-	-	-	-	1	4
READY (O) L	C4	-	-	-	-	-	2	3
READY 8 (I) L	D3	-	-	-	-	1	-	-
AST H	C4	-	-	-	-	-	3	-
SAC RL L	A2	-	-	-	-	-	1	2
SOFT CYCLE L	C6	-	-	-	-	-	1	1
START H	C1	-	-	-	-	-	2	1
TLED H	A2	-	-	-	-	-	2	1
UACLO (I) L	D7	-	-	-	-	1	-	-
WC CLK H	B2	-	-	-	-	-	1	-
WGO H	D3	-	-	-	-	-	1	-
WGO L	D3	-	-	-	-	-	2	2
CY INH (O) H	B1	-	-	-	-	-	2	1
MNCYC DLY H	G3	-	-	-	-	-	1	-

UD SOURCE	UD						
DO 15 L	AS	-	-	-	-	-	-
DO 14 L	AS	-	-	-	-	-	-
DO 13 L	AS	-	-	-	-	-	-
DO 12 L	AS	-	-	-	-	-	-
DO 11 L	BS	-	-	-	-	-	-
DO 10 L	BS	-	-	-	-	-	-
DO 9 L	BS	-	-	-	-	-	-
DO 8 L	BS	-	-	-	-	-	-
DO 7 L	CS	-	-	-	-	-	-
DO 6 L	CS	-	-	-	-	-	-
DO 5 L	CS	-	-	-	-	-	-
DO 4 L	CS	-	-	-	-	-	-
DO 3 L	CS	-	-	-	-	-	-
DO 2 L	CS	-	-	-	-	-	-
DO 1 L	CS	-	-	-	-	-	-
DO 0 L	CS	-	-	-	-	-	-
F2 L	CS	-	-	-	-	-	-
FNCT 1 (1) H	CS	-	-	-	-	-	-
FNCT 1 (1) L	CS	-	-	-	-	-	-
FNCT 2 (1) H	CS	-	-	-	-	-	-
FNCT 2 (1) L	CS	-	-	-	-	-	-
FNCT 3 (1) H	CS	-	-	-	-	-	-
FNCT 3 (1) L	CS	-	-	-	-	-	-
INT ENH (1) H	CS	-	-	-	-	-	-
INT ENH (1) L	CS	-	-	-	-	-	-
LOAD CSR HIGH C1	C1	-	-	-	-	-	2
LOAD CSR HIGH C1	C1	-	-	-	-	-	2
LOAD CSR LOW H C1	C1	-	-	-	-	-	1
LOAD CSR LOW L C1	C1	-	-	-	-	-	1
MASHT A (0) H	B2	-	-	-	-	-	-
MASHT A (1) H	B2	-	-	-	-	-	-
MASHT A (1) L	B2	-	-	-	-	-	-
MASHT B (0) H	B1	-	-	-	-	-	-
MASHT B (1) H	B1	-	-	-	-	-	-
WAB INC ENH A	A3	-	-	-	-	-	-
WCB INC CTL H	B2	-	-	-	-	-	-
WCI CTL H	B1	-	-	-	-	-	3
WCI CTL L	B1	-	-	-	-	-	1
W STATUS A H	B2	-	-	-	-	-	-
W STATUS B H	B2	-	-	-	-	-	-
W STATUS C H	B2	-	-	-	-	-	-
WWC INC ENH MAZ	A2	-	-	-	-	-	-

<u>DB Source</u>	<u>C1</u>	<u>C2</u>	<u>C3</u>	<u>C4</u>	<u>C5</u>	<u>C6</u>	<u>C7</u>	<u>C8</u>
BR MASTER H	A3	-	-	-	-	-	-	5
BR MASTER L	B4	-	-	-	-	-	-	3 3
BR SACK M	B4	-	-	-	-	-	-	1 1
INTERRUPT D	H	B6	-	-	-	-	-	1 1
INTERRUPT I	H	B6	-	-	-	-	-	1 2
NPR MASTER L	D4	-	-	-	-	-	-	2
WPA L	A1	-	-	-	-	-	-	1 1
WPG H	A1	-	-	-	-	-	-	1 1

REVERSE		REV.
CHEK	CHARGE NO.	

AUTOMATED BY EFTI ST-31 (32)

P A R T S      L I S T

SHEET A1 OF A3

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	SHEET A1 OF A
				00		REFERENCE DESIGNATOR
1	1	D-MD-5013369-0-0	5013369-00	DR11W		
2	2		1012784-00	.047 MFD 50V +80-20%	1	
3	3		1005306-00	6.8MFD 35V 10% CER	42	C14-C55
4	4		1209838-00	SOCKET 16PIN S.TANT	6	C1-C6
5	5		1209941-02	HEADER 100 40POS RT ANGLE	1	XE62
6	6		1209941-03	HEADER RT ANGLE LEFT L	2	J1,J2
7	7		1209941-04	HEADER RT ANGLE,RIGHT	2	
8	8		1210711-02	/REPLACED BY 12-16908-02	1	
9	9		9009000-00	EYELET, ROLLED FLANGE, .121 OD X	10	
10	10		1211164-01	SW,DIP 1P 1A 5POS	1	
11	11		1211164-04	SW,DIP 1P 1A 8POS	1	E105
12	12		1211164-06	SW,DIP 1P 1A 10POS	1	E15
13	13		1210209-00	SW,TOG,SPDT,.01A@6V,ON/ON,SUBMIN	1	E120
14	14		1301322-00	180.0 .25 W 5.0 % CC	57	S1 R5-R20,R37-R43,R62-R72, R120-R122,R88-R103,R81,R82,R84
15	15		1300309-00	390.0 .25 W 5.0 % CC	57	CONT CONT R86
16	16		1300005-01	R NETWORK 13-1K 5.0 % 14PIN	2	
17	17		1611197-00	DELAY=50-500NS,10TAPS	2	E119,E22
18	18		1611327-00	DELAY= 125NS,0TAPS	2	E100,E112
19	19		1300391-00	1.50 K .25 W 5.0 % CC	2	E110,E121
20	20		1300432-00	3.0 K .25 W 5.0 % CC	2	R1,R78
21	21		1300295-00	330.0 .25 W 5.0 % CC	2	R2,R79
22	22		1301424-00	680.0 .25 W 5.0 % CC	1	R3
23	23		1311522-00	200.0 .25 W 5.0 % CC	4	R4
24	24		1312930-00	5.10 K .25 W 5.0 % CC	3	R73,R74,R76,R77
25	25		1302177-00	47.0 K .25 W 5.0 % CC	2	R85,R126,R127
26	26		1309143-13	50.0 K .75 W10.0 % POT	1	R75,R129
27	27		1000027-00	820.0 MMF 100V 5%200PPM MICA	1	R80 C7

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LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR	
				00		
28	28	SEE NOTE	1000055-00 10C4812-00 1000015-00 1110864-00 1910651-00 1912847-00 1910951-00 9008085-00 1910547-00 1914438-00 1905547-00	2200.0 MMF 250V 20% Y5S DISC 15 MFD 20V 10% S.TANT 82.0 MMF 100V 5%200PPM MICA LED 2MC10@10MA DEC 74175 FF-D QUAD LS157 MUX 1 OF 2(QUAD) 9602 ONE SHOT-DUAL TERM PCB 1POS SOLDER TURRET 74S153 MUX 1 OF 4 (DUAL) DC 013 UNIBUS INTERRUPT-BIP 7474 FF-D DUAL,EDGE TRIGG	2 1 2 2 4 3 3 1 8 2 9	C8,C9 C13 C11,C12 D1,D2 E2,E4,E6,E10 E66,E67,E69 E78,E61,E83 TF1 E45,E46,E52,E64,E53,E54,E55,E56 E106,E107 E127,E80,E90,E104,E87,E102,E92, CONT E79,E125 E115,E124,E68 CONT E24,E25,E26,E27,E31,E32,E33,E34, E28,E57 E9,E14,E17,E19,E47,E48,E49 E72 E44 E16,E58,E21 E116 E40,E23 E117,E118 E75 E89 E122 E91 E42 E65 E81,E77 E59,E76,E70,E111,E108,E113,E109, CONT E114 E60,E43,E93,E98,E8 E97,E99 E88,E123,E101,E103,E71 E12,E18,E13,E20,E35,E36,E39 E3,E5,E7,E11,E30,E29,E41,E51, CONT E96,E63,E95,E73,E84,E85,E74 E94,E82 E37,E38,E50 E126 C10 R131 E62 R132 C56 E86
39	40	1912820-00	LS51 A-0-I GATE 2-WIDE 2I	3		
40	41	1910018-00	DEC 74193 COUNTER,SYNCHR. UP/D	10		
41	42	1912848-00	LS158 MUX 1 OF 2 (QUAD)	7		
42	43	1911676-00	74S139 DECODER-DUAL TWO-INF	1		
43	44	1912821-00	LS54 A-0-I GATE,3-2-2-3IN	1		
44	45	1912863-00	LS273 FF-D OCTAL W/CLEAR	3		
45	46	1910011-00	DEC 7486 X-OR GATE-QUAD 2INPU	1		
46	47	1910548-00	74S157 MUX 1 OF 2 (QUAD)	2		
47	48	1912395-00	DM 8136 COMPARATOR-6BIT UNIF	2		
48	49	1912811-00	LS21 AND GATE-DUAL 4IN,PO	1		
49	50	1910541-00	74S40 NAND GATE-DUAL 4IN,B	1		
50	51	1912746-00	DEC 74S37 NAND GATE-QUAD 2IN	1		
51	52	1910537-00	74S11 AND GATE-TRIPLE 3INF	1		
52	53	1910536-00	74S10 NAND GATE-TRIPLE 3IN	1		
53	54	1912389-00	74S08 AND GATE-QUAD 2IN,PO	1		
54	55	1910155-00	DEC 7408 AND GATE,POS.QUAD 2I	2		
55	56	1909686-00	7404 INVERTER GATE-HEX 1I	8		
56	57	1905575-00	7400 NAND GATE-QUAD 2IN	5		
57	58	1910878-00	7427 NOR GATE-TRIPLE 3IN	2		
58	59	1909004-00	DEC 7402 NOR GATE-QUAD 2IN	5		
59	60	1911469-00	DEC 8640 RECEIVER,BUS,QUAD,U	7		
60	61	1911579-00	8641 TRANSCEIVER,BUS,QUA	15		
61	62	1911521-00	7432 OR GATE-QUAD 2IN, PO	2		
62	63	1909705-00	DEC 8881 NAND GATE-QUAD 2IN O	3		
63	64	1910539-00	74S20 NAND GATE-DUAL 4INPU	1		
64	65	1001610-00	.01 MFD 50V +80-20% 25U CER	1		
65	66	1300365-00	1.0 K .25 W 5.0 % CC	1		
66	67	540C778-00	PLUG PRIORITY	1		
67	68	1300479-00	10.0 K .25 W 5.0 % CC	1		
68	69	1000020-00	180.0 MMF 100V 5%200PPM MICA	1		
69	70	1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1		
70	71	9105740-55	WIRE(WRAP)30AWG UL1423 A/R			

AUTOMATED BY PRTLST.3L(37)

## PARTS LIST

SHEET A3 OF A3

**LINE ITEM DOCUMENT NUMBER**

**PART NUMBER**

**DESCRIPTION**

QTY PER VARIATION  
00

**REFERENCE DESIGNATOR**

71 NOTE: C8 IS NOT FOR VOLUME PRODUCTION IT IS USED FOR TIME OUT ON 11/780

! ! ! ! ! ! ! TITLE						! ! ! ! ! ! !				
D	I	G	I	T	A	L	DR11-W	! SECTION A OF A	! SIZE ! CODE ! DOCUMENT NUMBER ! REV	
									K PL MB716-0-DBP	F

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### WIRE TABLE

FROM	TO
P1-A	P2-VV
P1-B	P2-WJ
P1-C	P2-TT
P1-D	P2-SS
P1-E	P2-RR
P1-F	P2-PP
P1-H	P2-NN
P1-J	P2-MM
P1-K	P2-LL
P1-L	P2-KK
P1-M	P2-JJ
P1-N	P2-HH
P1-P	P2-FF
P1-R	P2-EE
P1-S	P2-DD
P1-T	P2-CC
P1-U	P2-BB
P1-V	P2-AA
P1-W	P2-Z
P1-X	P2-Y
P1-Y	P2-X
P1-Z	P2-W
P1-AA	P2-V
P1-BB	P2-U
P1-CC	P2-T
P1-DD	P2-S
P1-EE	P2-R
P1-FF	P2-P
P1-GH	P2-N
P1-JJ	P2-M
P1-KK	P2-L
P1-LL	P2-K
P1-MM	P2-J
P1-NN	P2-H
P1-PP	P2-F
P1-RR	P2-E
P1-SS	P2-D
P1-TT	P2-C
P1-JU	P2-B
P1-VV	P2-A

A

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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9

B

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

7

8

C

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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D

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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F

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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G

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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I

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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M

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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P

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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Q

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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R

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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S

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

-

-

T

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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U

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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V

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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W

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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X

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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Z

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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A

REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISIONS  
CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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CHG CHANGE NO REV  
P/A BC05L-00001 A  
REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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CHG CHANGE NO REV  
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REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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REVISED & REDRAWN 7-7-72  
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CHG CHANGE NO REV  
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REVISED & REDRAWN 7-7-72  
DWG NO WAS DUA-BC05L-00001

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**DIGITAL EQUIPMENT CORPORATION**  
**MAYNARD, MASSACHUSETTS**  
**PARTS LIST**

MADE BY A. COLON  
DATE 4 OCT 74

DATE 19-10-79  
ENG W.M. Lang  
DATE 19-10-79

BRUNSWICK BROS. & CO., NEW YORK.

CHECKED 3. B. Rives Jr  
8-27-73

DATE 8 Oct 79

**PROD** *W. Elsnapp*  
**PAGE** *14 Jan 1979*

DATE 18 OCT 1991

## SECTION

1

**ISSUED SECT**

1

**TITLE**

DR11-W SHIPPING LIST

ASSY NO.

D=UA=DR11-W-Ø

**SIZE** | **CC**

A P

NUMBER

DR11-

R

EV ECO NO.  
A DR11-W  
MT. 001